

ABSTRACT OF THE DISCLOSURE

A primary/secondary surge protector circuit for protecting telecommunications equipment from power and surge transients includes a printed circuit board and a surge protector circuit. The surge 5 protector circuit is mounted on the printed circuit board. The printed circuit board has tip and ring conductive traces formed on its top surface. The surge protector circuit includes a voltage suppressor operatively coupled to the tip and ring conductive traces. The tip and ring conductive traces define fusible links which are 10 opened when an excessive current is passed through so as to meet the specifications set forth by UL Standards 497 and 497A.

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